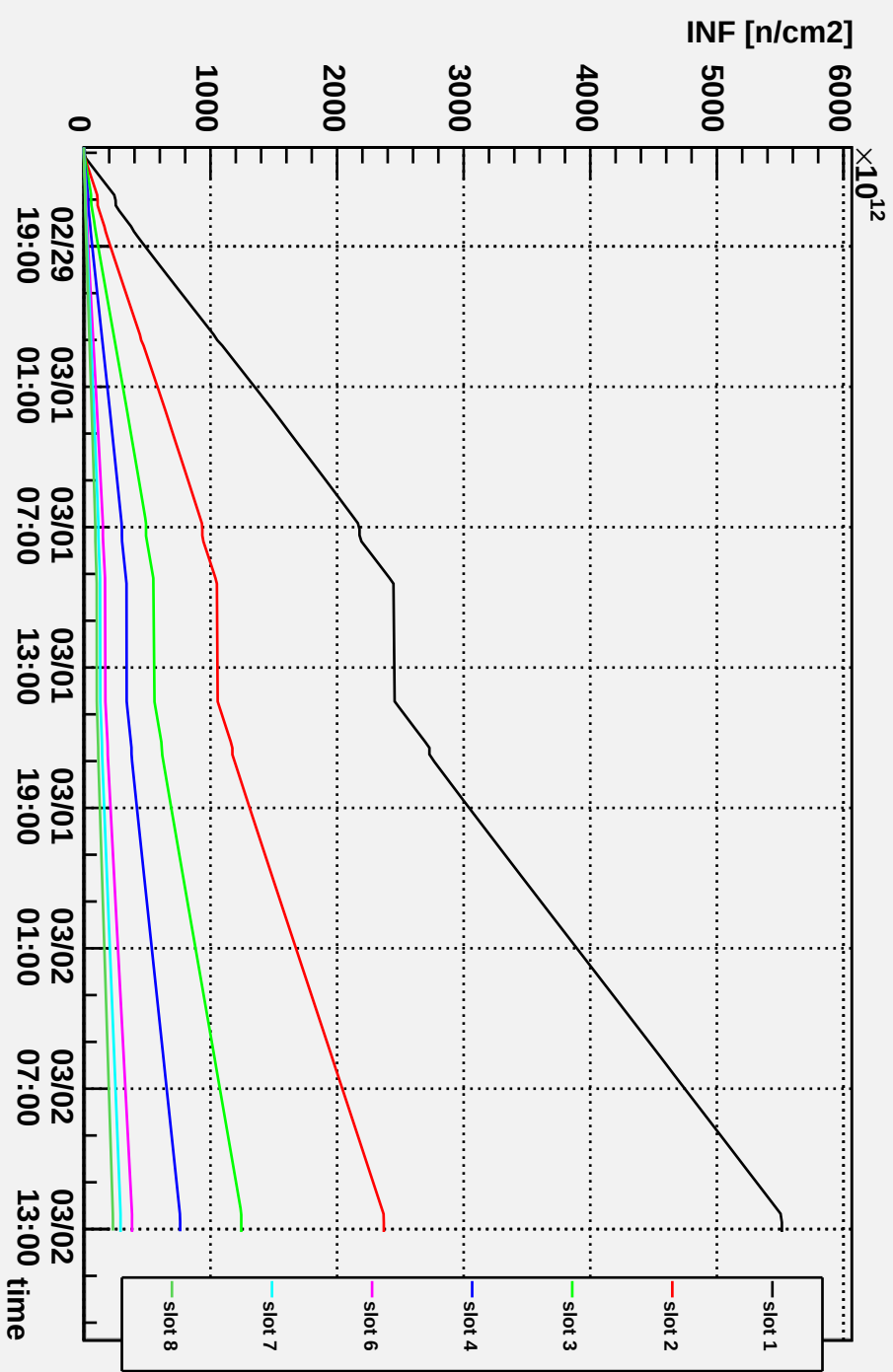
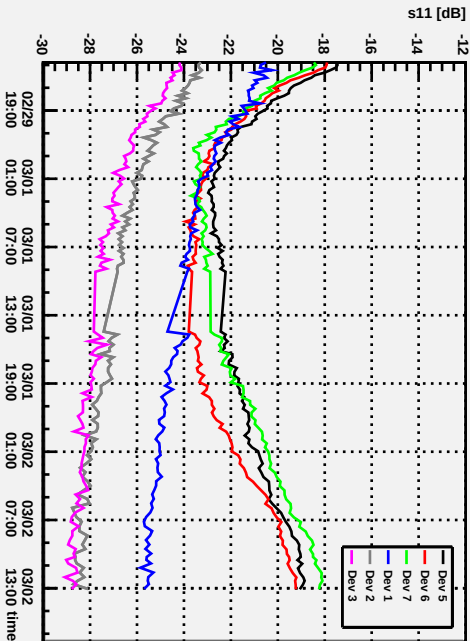


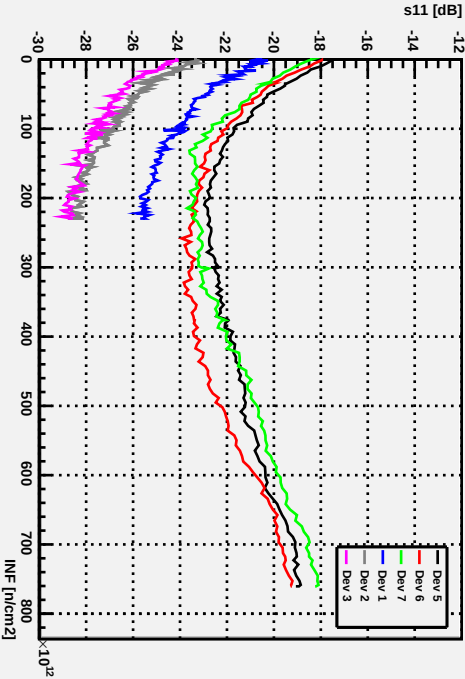
INF vs. time for all slot positions (slot 5 is free)



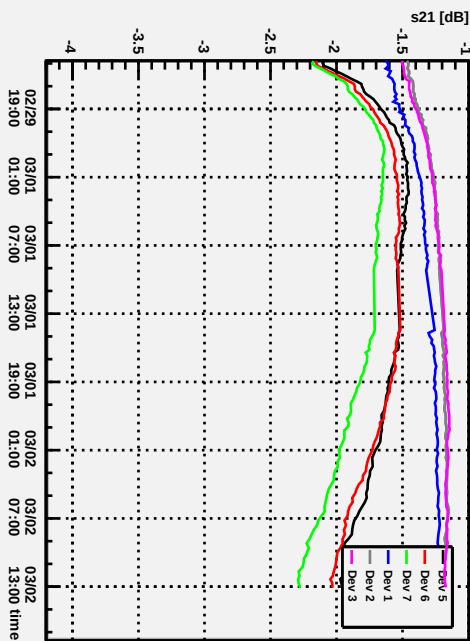
S11 vs. time, at 20 MHz, HEC PA's, slot 8 & 4)



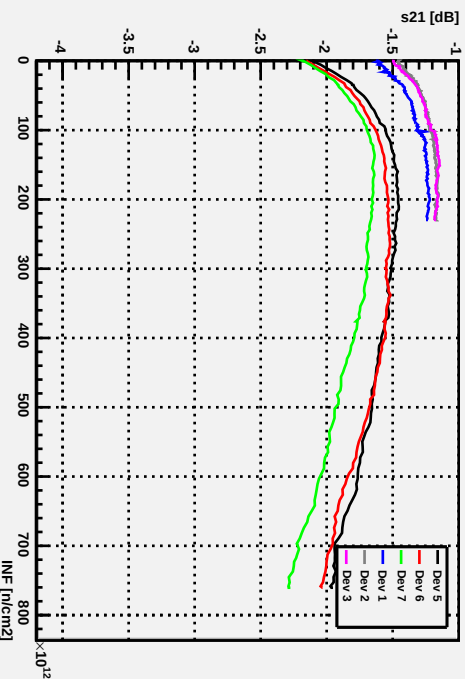
S11 vs. INF, at 20 MHz, HEC PA's, slot 8 & 4)



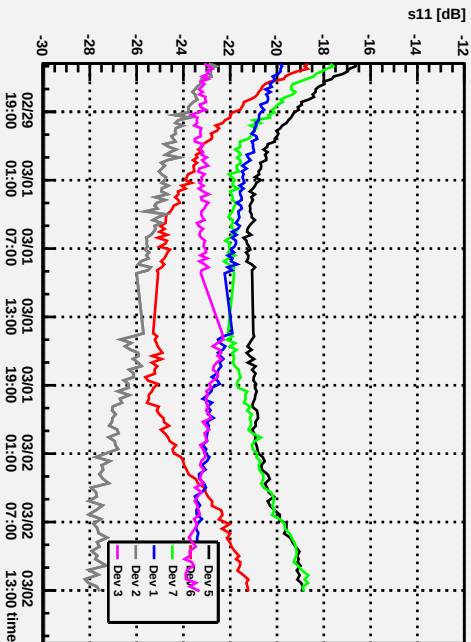
S21 vs. time, at 20 MHz, HEC PA's, slot 8 & 4)



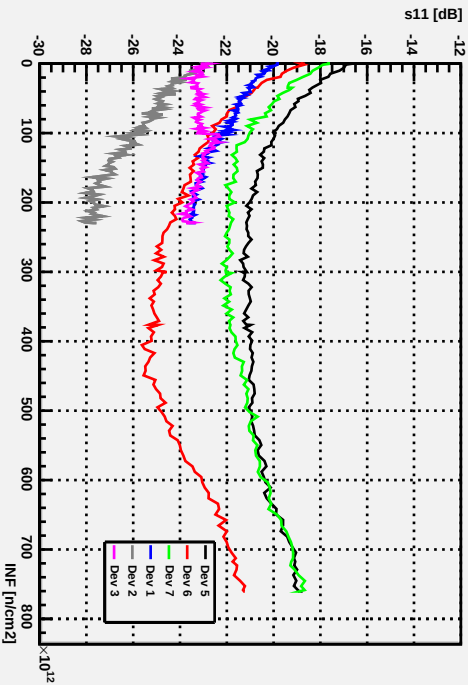
S21 vs. INF, at 20 MHz, HEC PA's, slot 8 & 4)



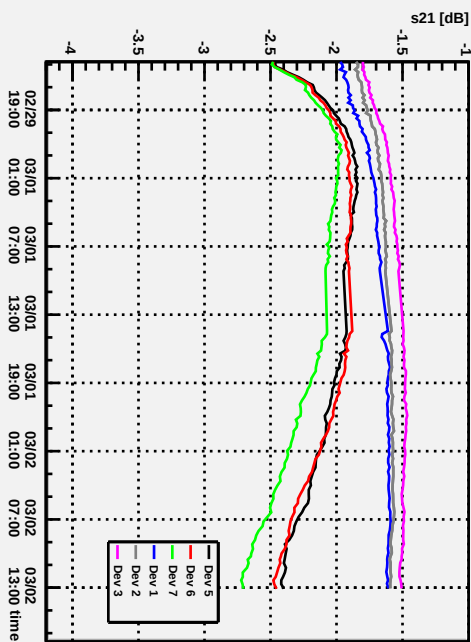
S11 vs. time, at 40 MHz, HEC PA's, slot 8 & 4)



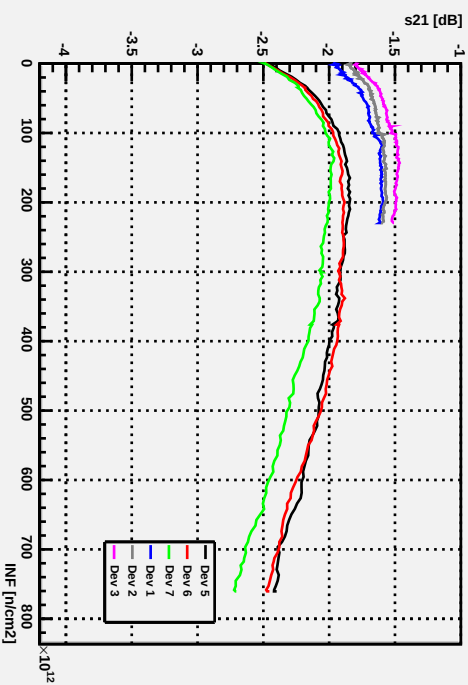
S11 vs. INF, at 40 MHz, HEC PA's, slot 8 & 4)



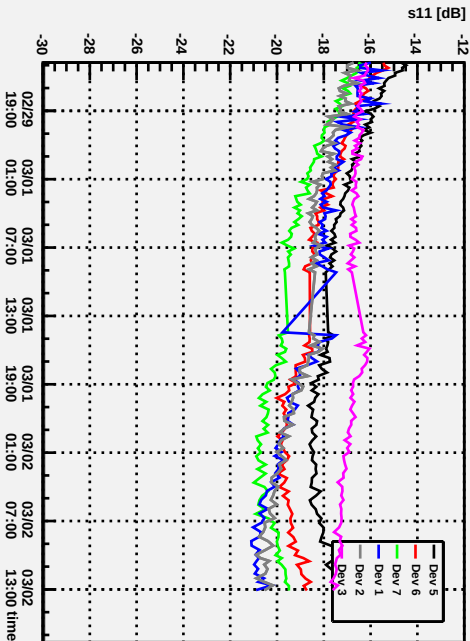
S21 vs. time, at 40 MHz, HEC PA's, slot 8 & 4)



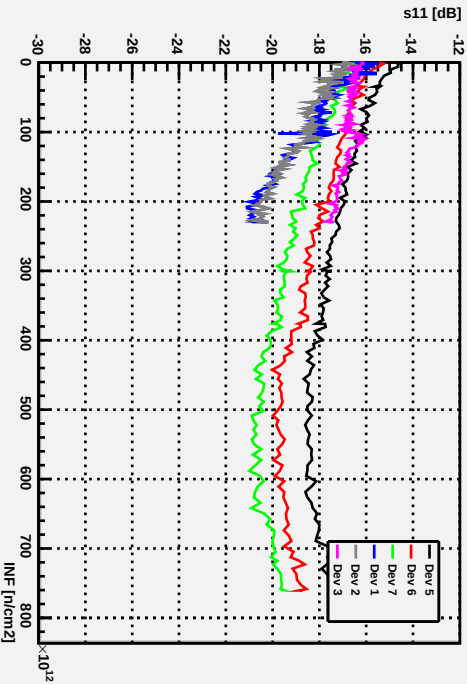
S21 vs. INF, at 40 MHz, HEC PA's, slot 8 & 4)



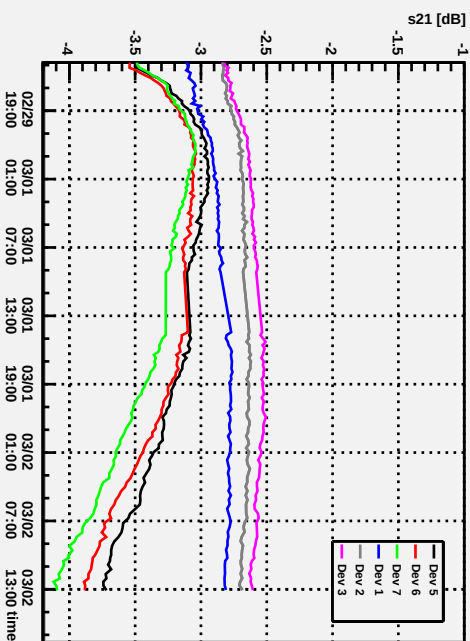
S11 vs. time, at 80 MHz, HEC PA's, slot 8 & 4)



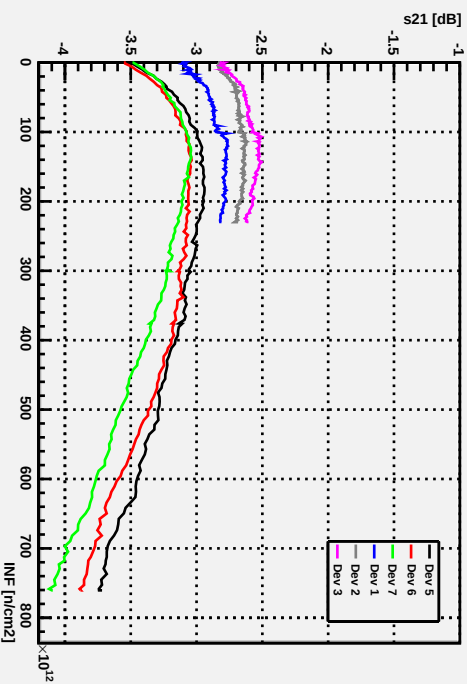
S11 vs. INF, at 80 MHz, HEC PA's, slot 8 & 4)



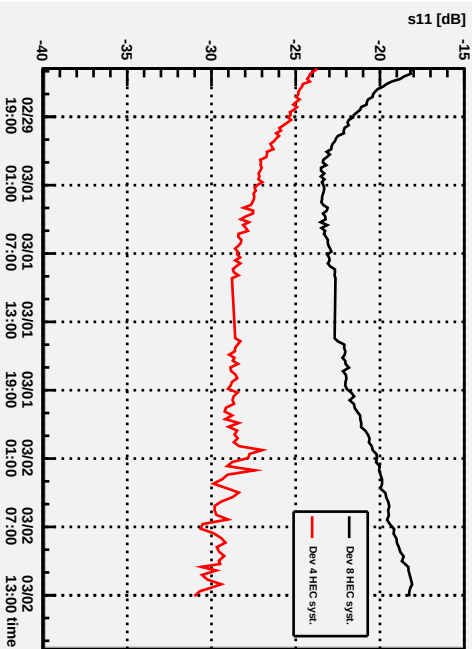
S21 vs. time, at 80 MHz, HEC PA's, slot 8 & 4)



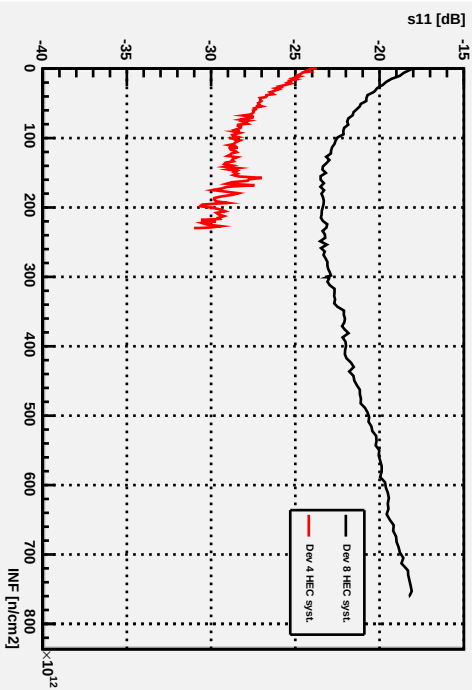
S21 vs. INF, at 80 MHz, HEC PA's, slot 8 & 4)



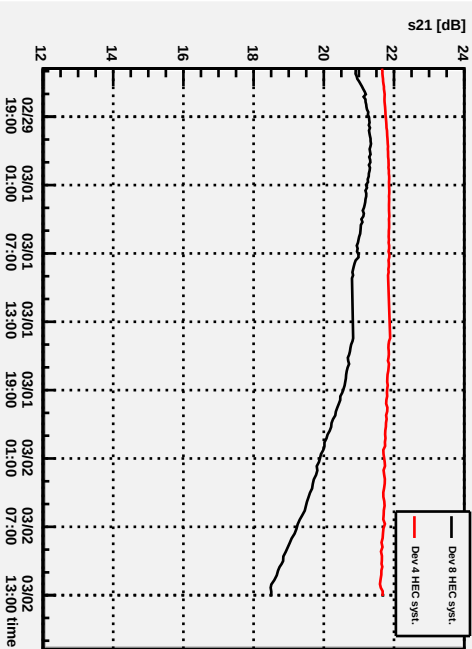
S11 vs. time, at 20 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



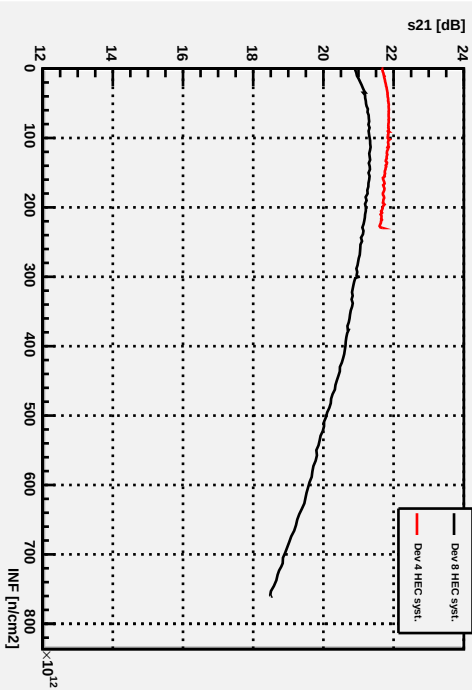
S11 vs. INF, at 20 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



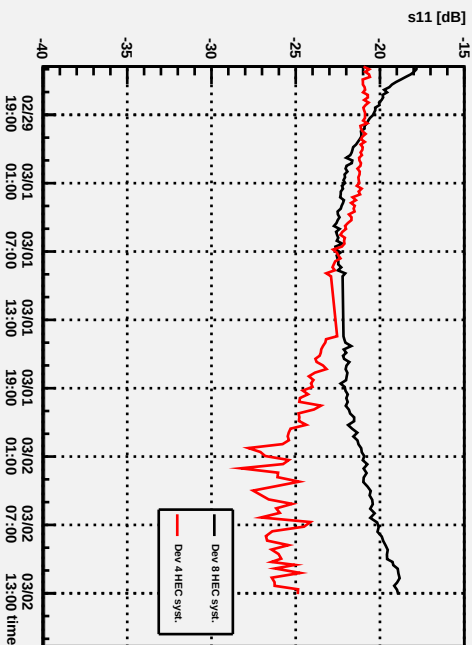
S21 vs. time, at 20 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



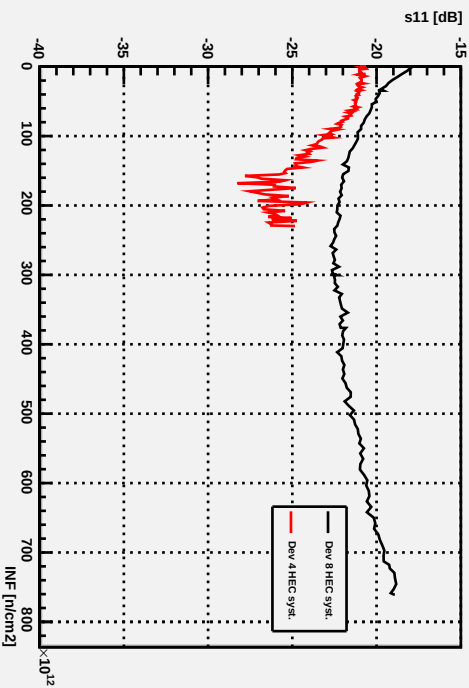
S21 vs. INF, at 20 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



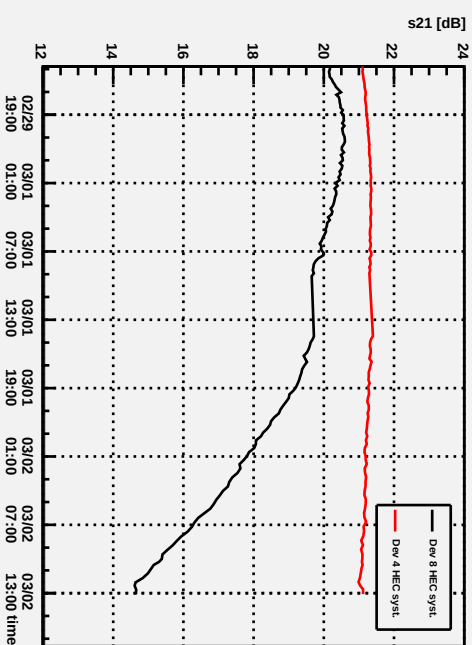
S11 vs. time, at 40 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



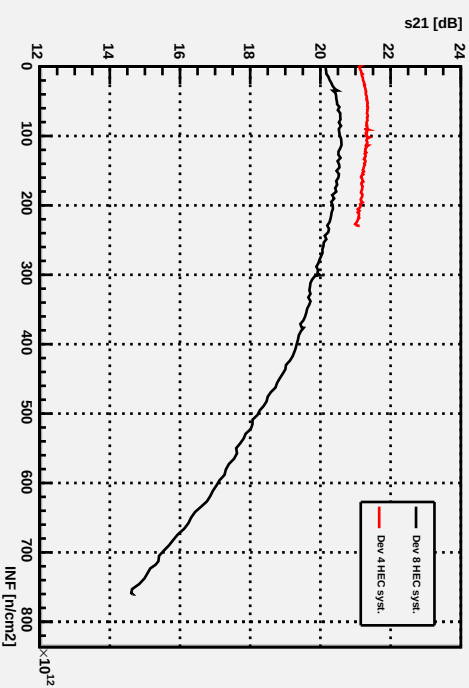
S11 vs. INF, at 40 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



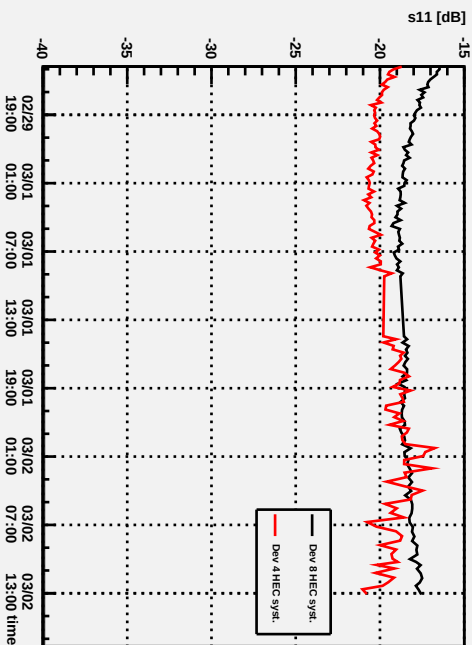
S21 vs. time, at 40 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



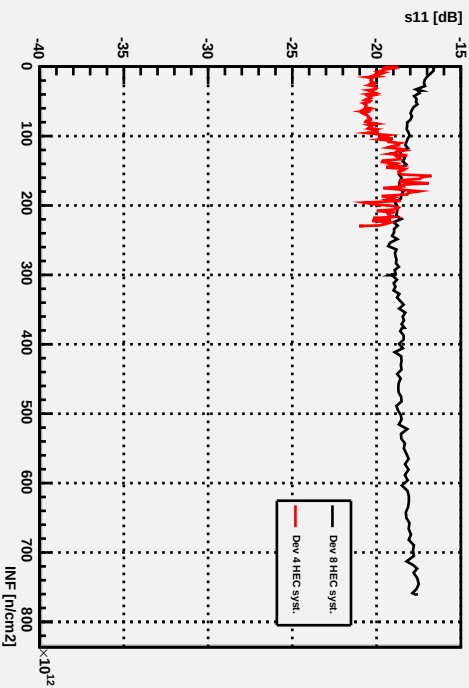
S21 vs. INF, at 40 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



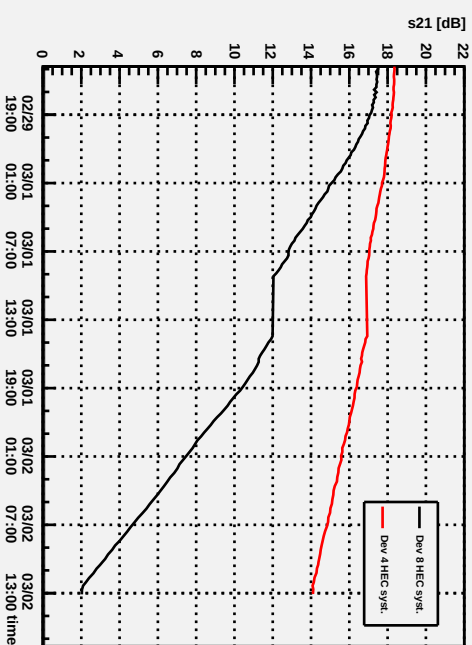
S11 vs. time, at 80 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



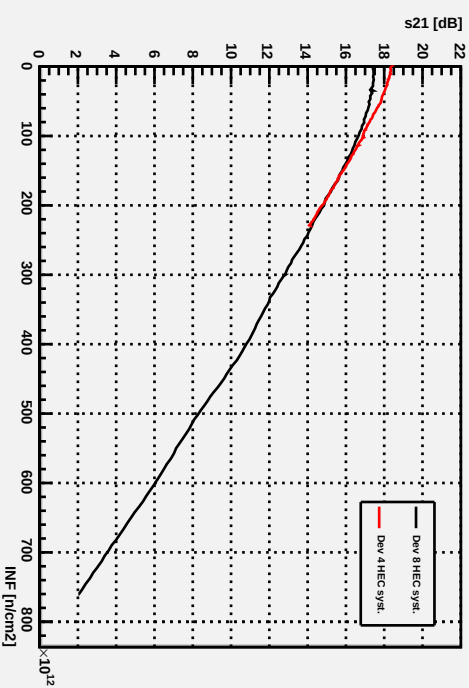
S11 vs. INF, at 80 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



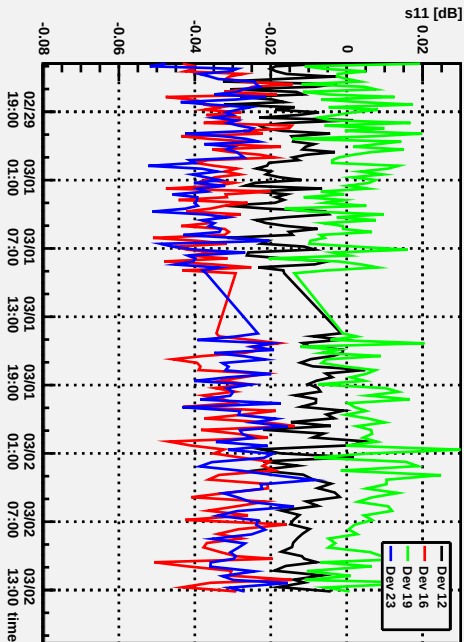
S21 vs. time, at 80 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



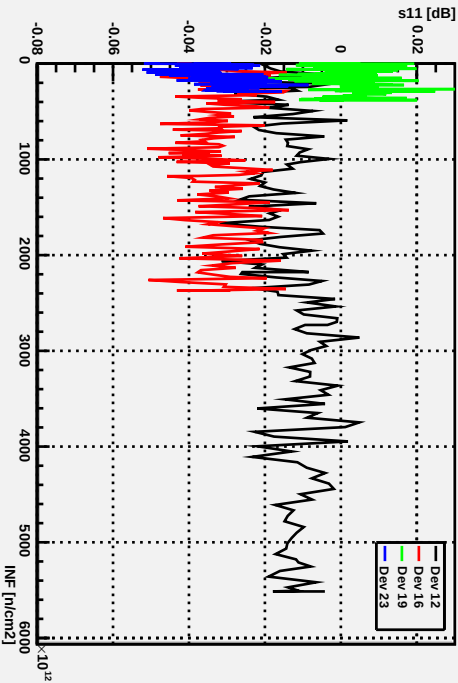
S21 vs. INF, at 80 MHz, Dev 4 & 8 (HEC syst., slot 8 & 4)



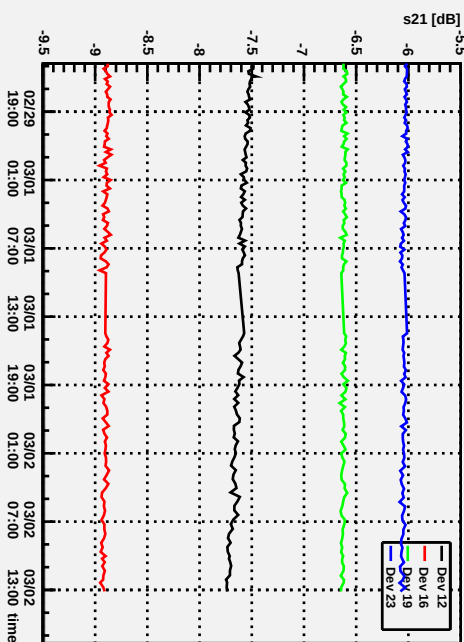
S11 vs. time, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



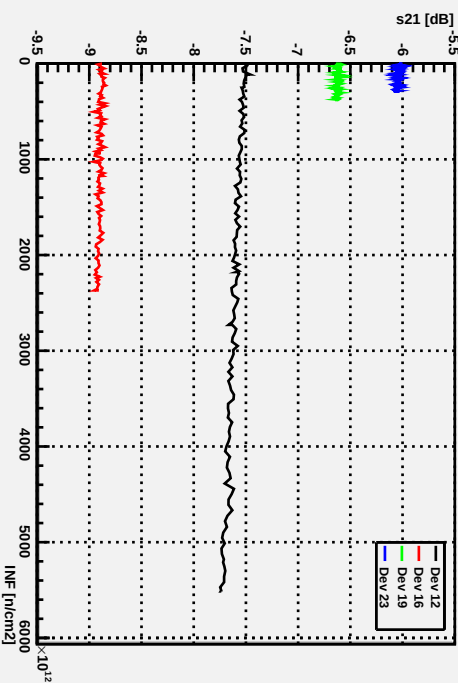
S11 vs. INF, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



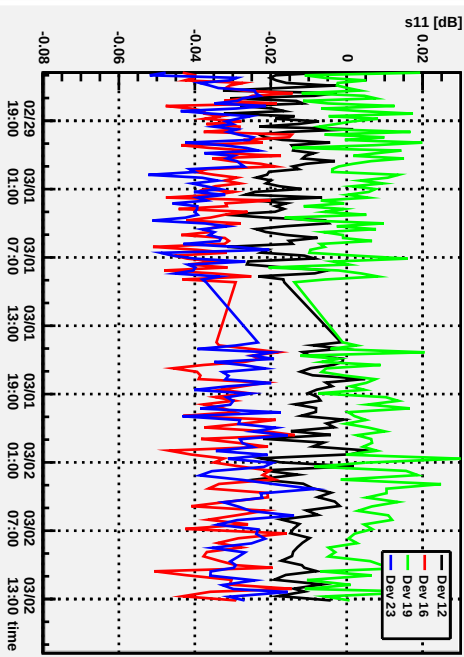
S21 vs. time, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



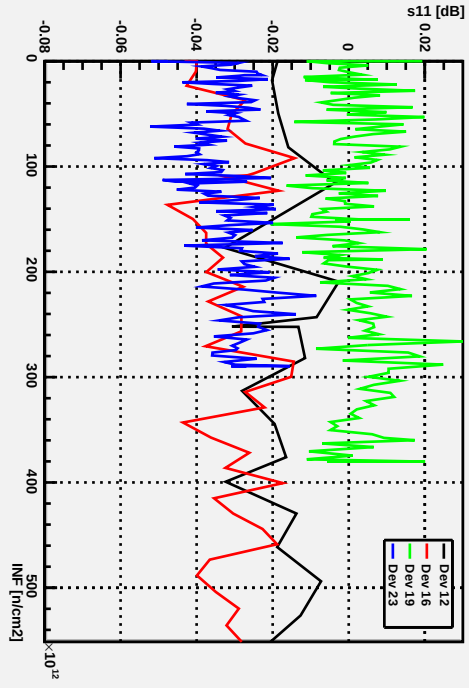
S21 vs. INF, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



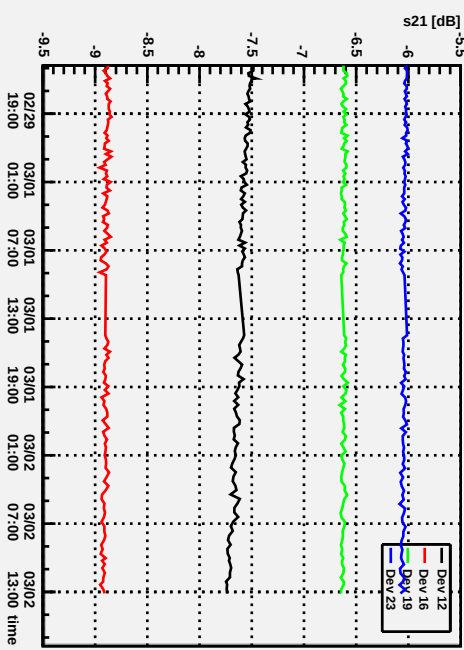
S11 vs. time, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



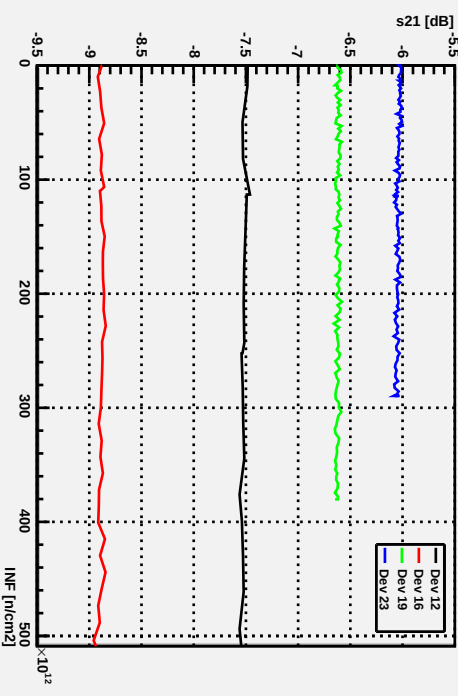
S11 vs. INF, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



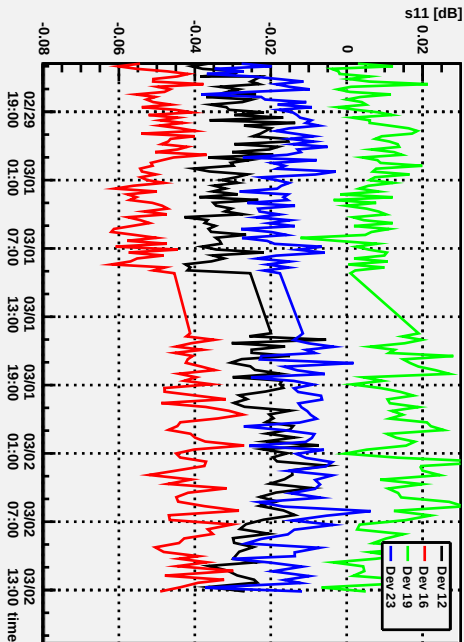
S21 vs. time, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



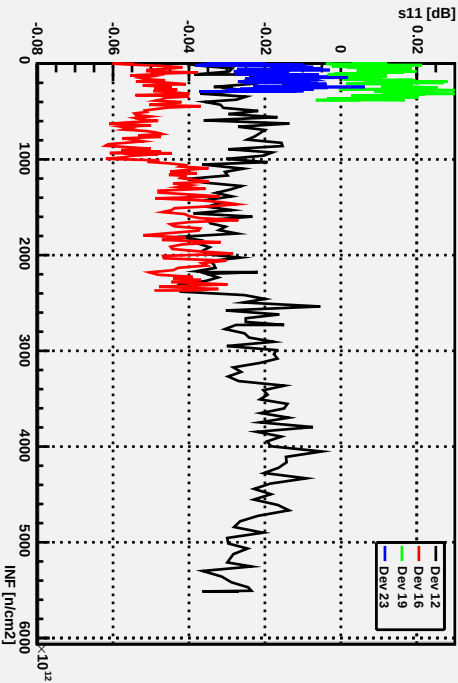
S21 vs. INF, at 20 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



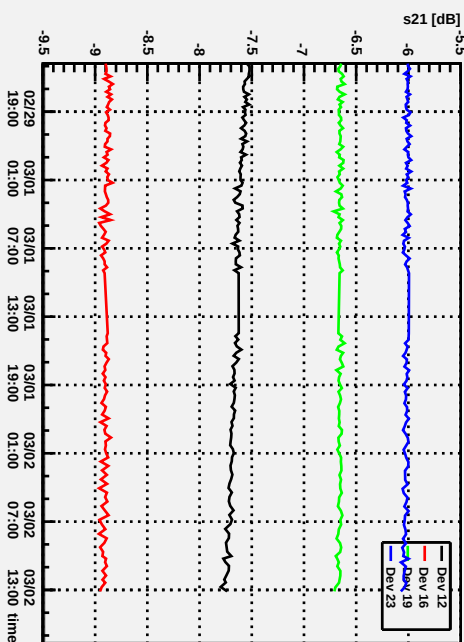
S11 vs. time, at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



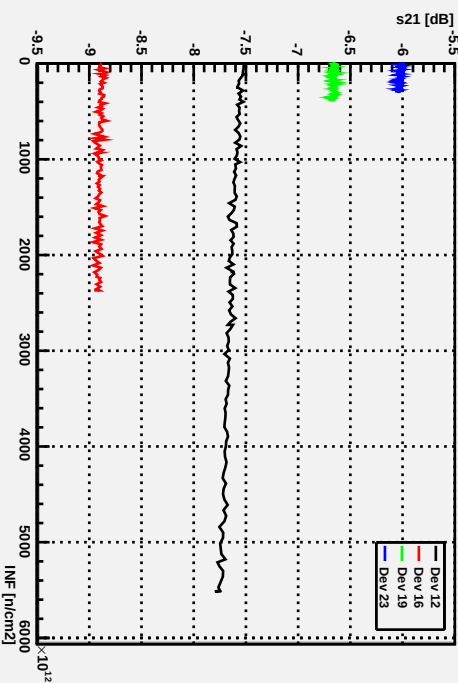
S11 vs. INF, at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



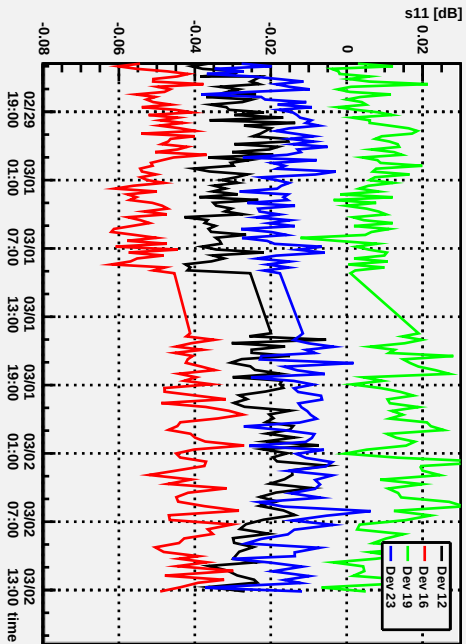
S21 vs. time, at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



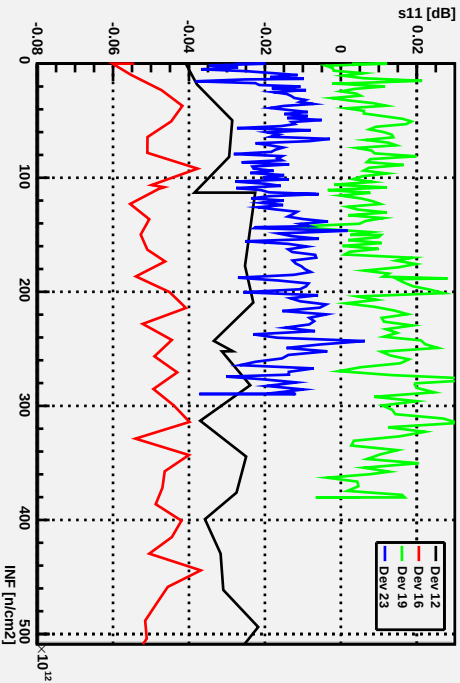
S21 vs. INF, at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



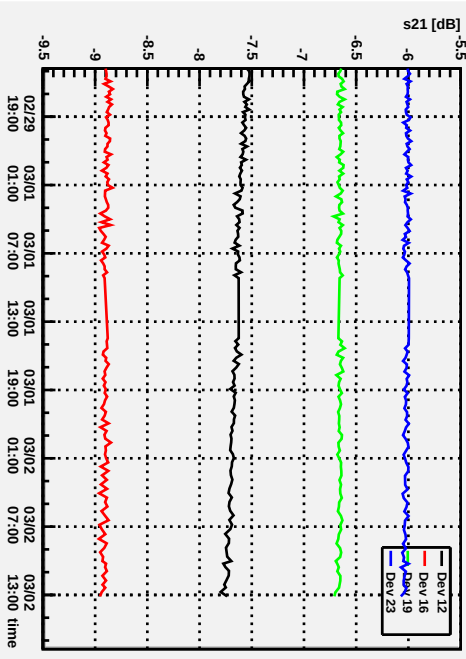
S11 vs. time, at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



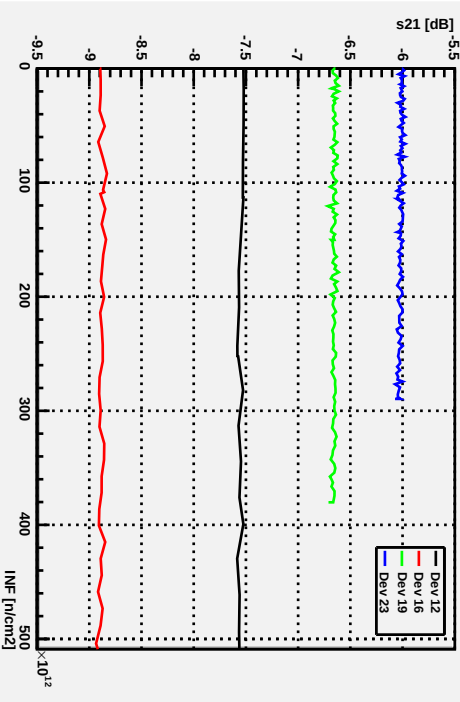
S11 vs. INF. at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



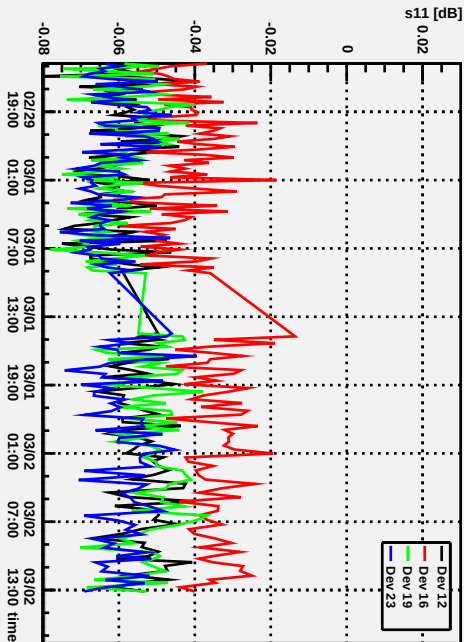
S21 vs. time, at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



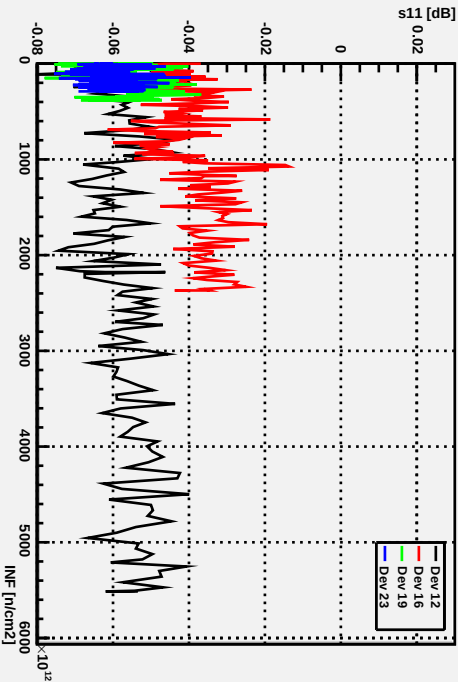
S21 vs. INF. at 40 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



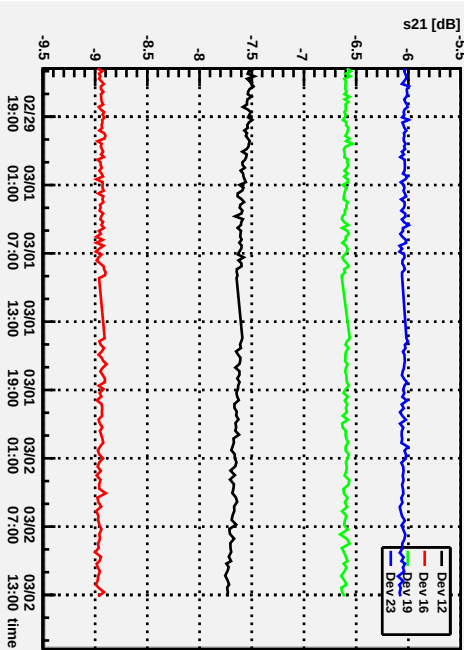
S11 vs. time, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



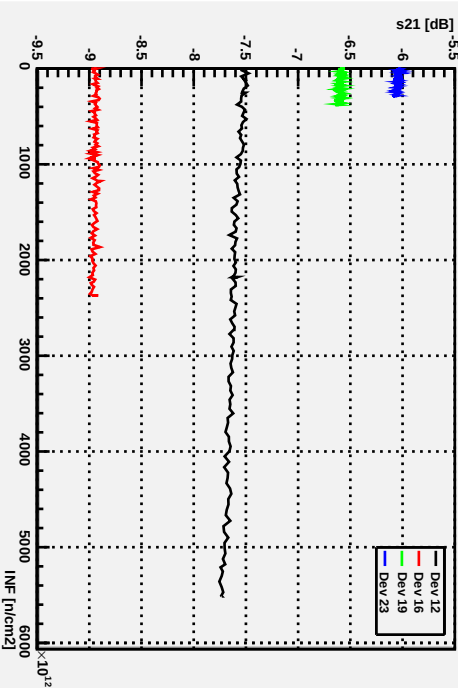
S11 vs. INF, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



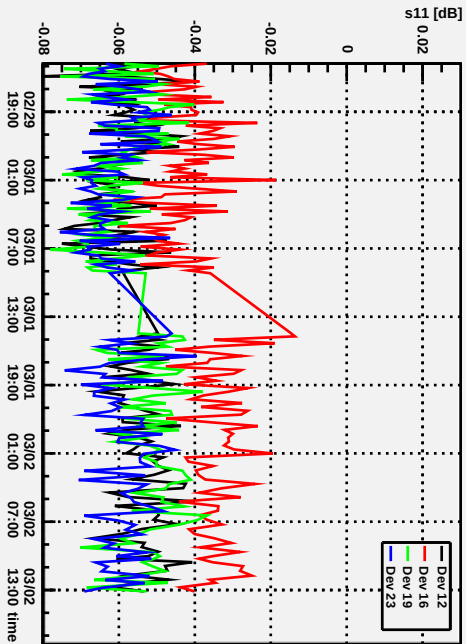
S21 vs. time, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



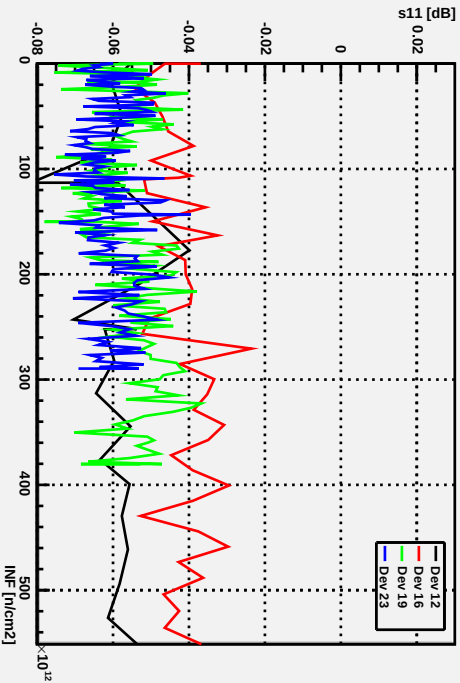
S21 vs. INF, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



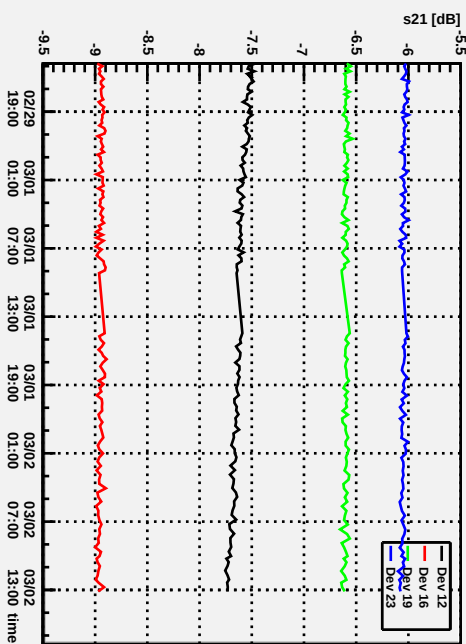
S11 vs. time, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



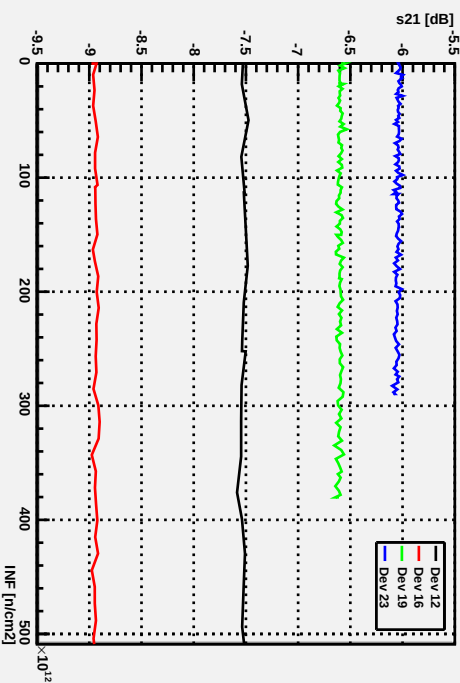
S11 vs. INF, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



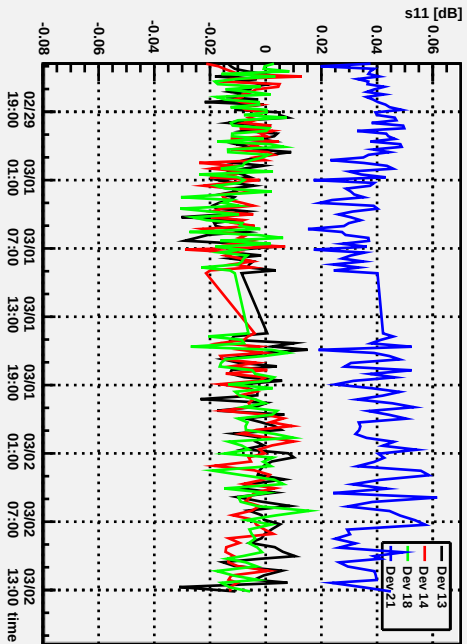
S21 vs. time, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



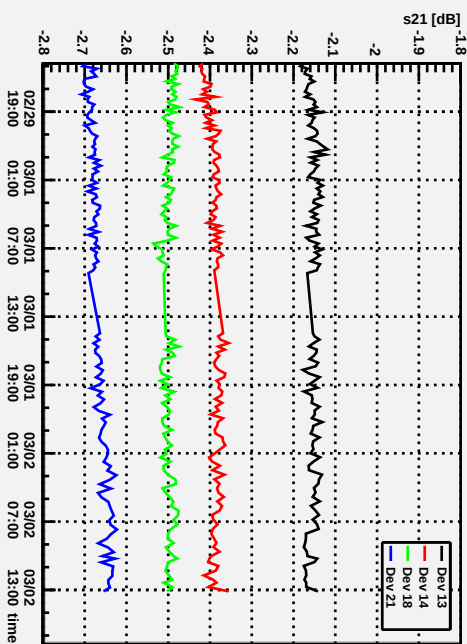
S21 vs. INF, at 80 MHz, Dev 12, 16, 19, 23 (IHP Sige trans., p-type)



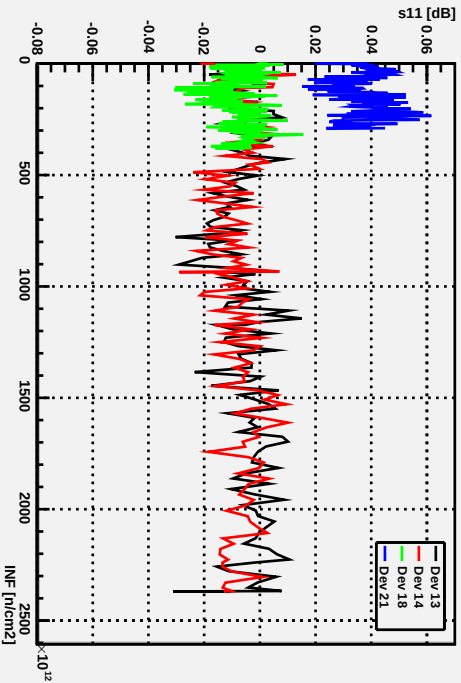
S11 vs. time, at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



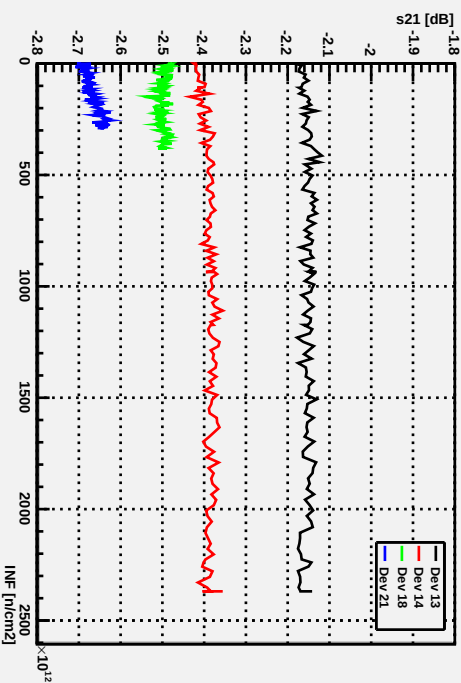
S21 vs. time, at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



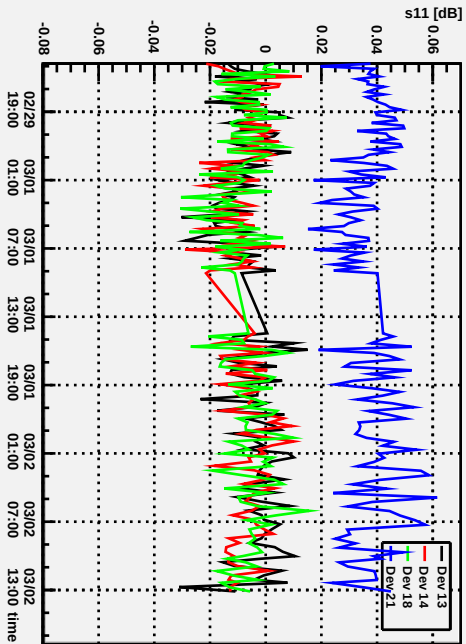
S11 vs. INF, at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



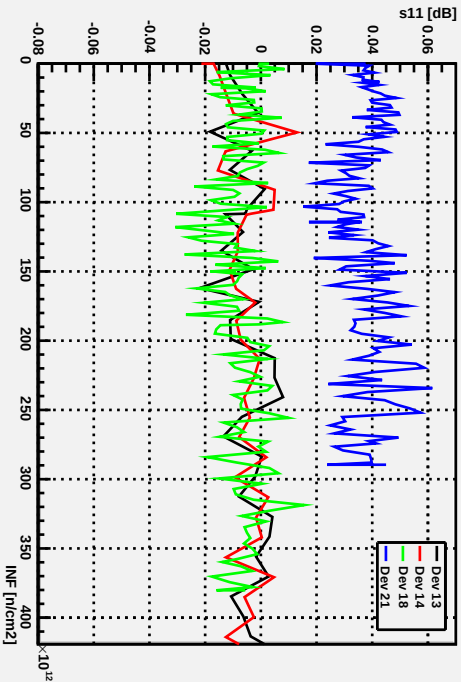
S21 vs. INF, at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



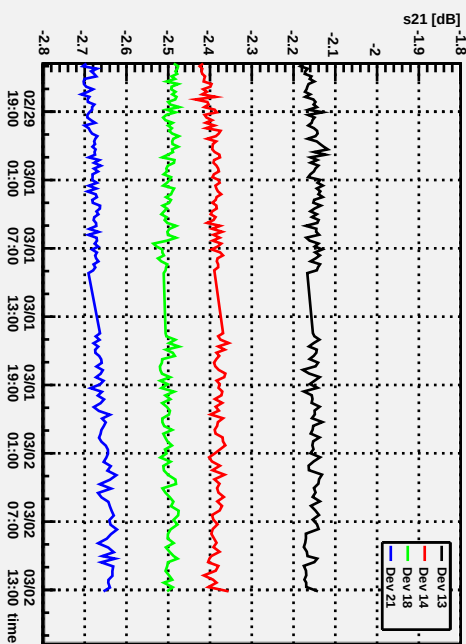
S11 vs. time, at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



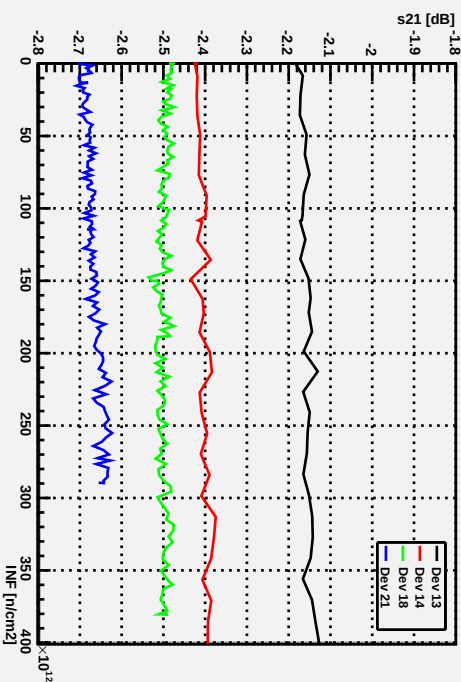
S11 vs. INF. at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



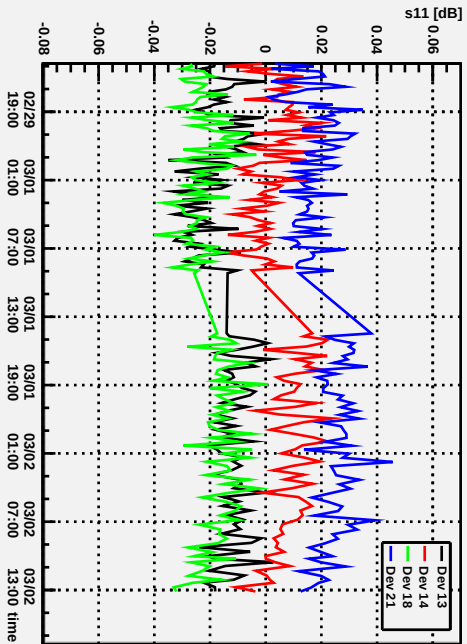
S21 vs. time, at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



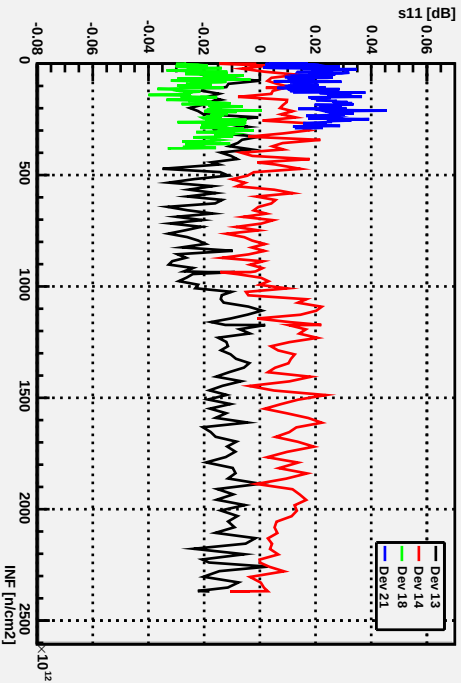
S21 vs. INF. at 20 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



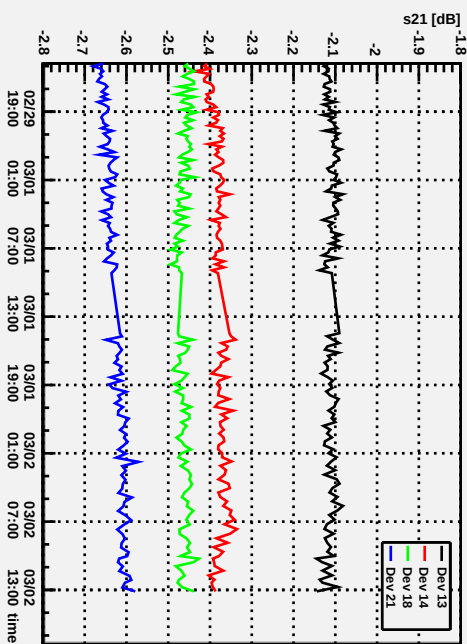
S11 vs. time, at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



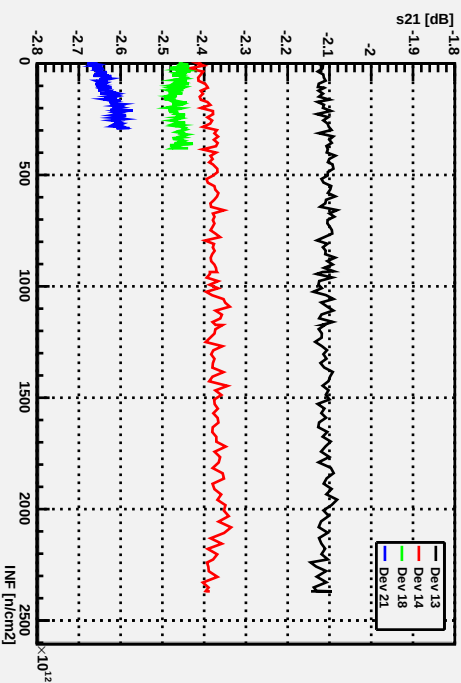
S11 vs. INF. at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



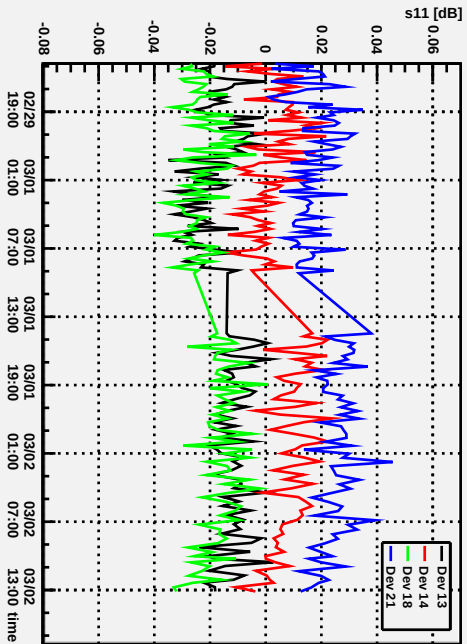
S21 vs. time, at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



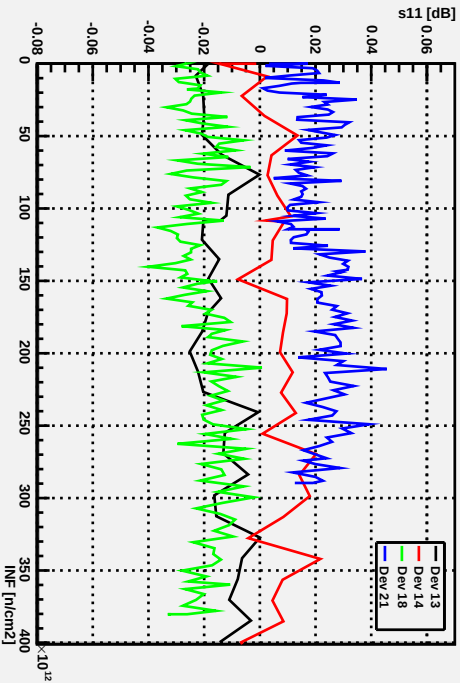
S21 vs. INF. at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



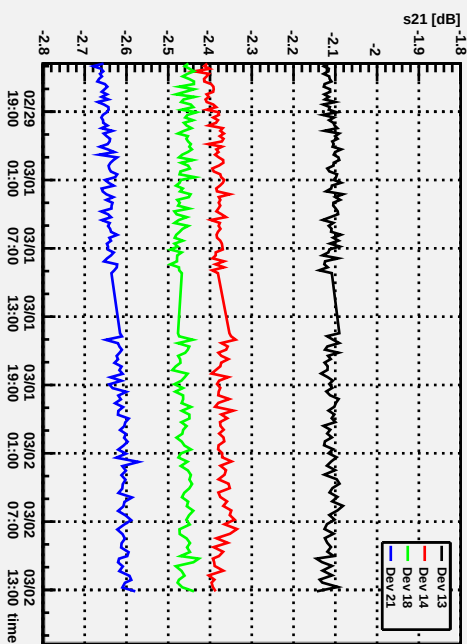
S11 vs. time, at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



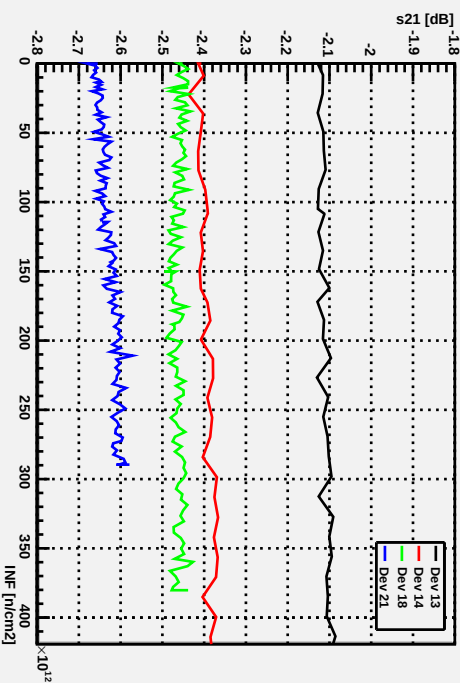
S11 vs. INF, at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



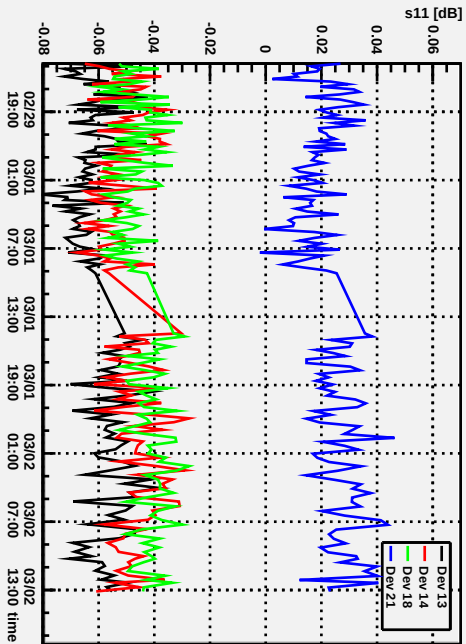
S21 vs. time, at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



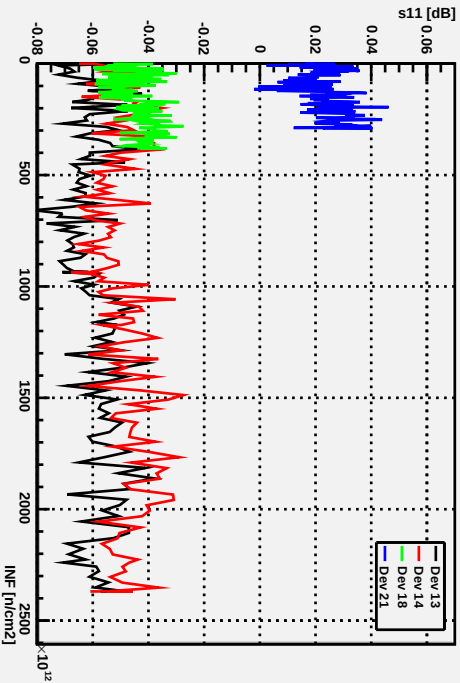
S21 vs. INF, at 40 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



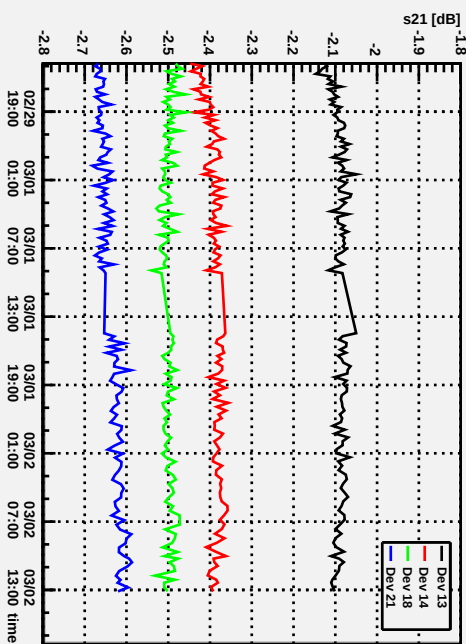
S11 vs. time, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



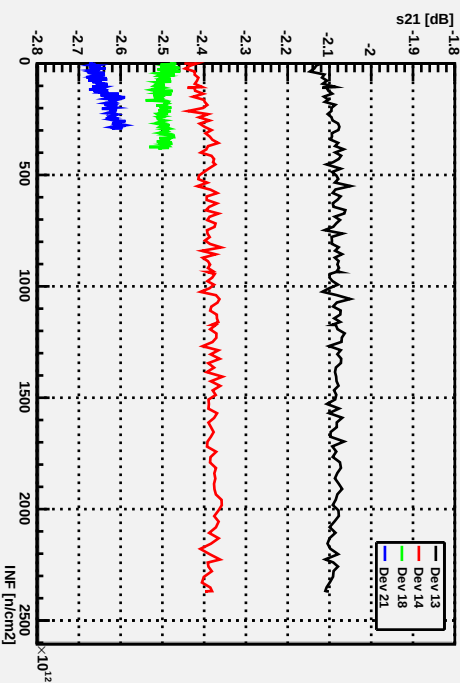
S11 vs. INF, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



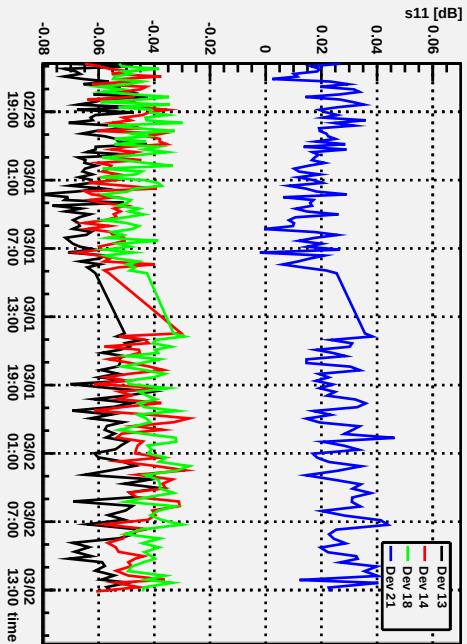
S21 vs. time, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



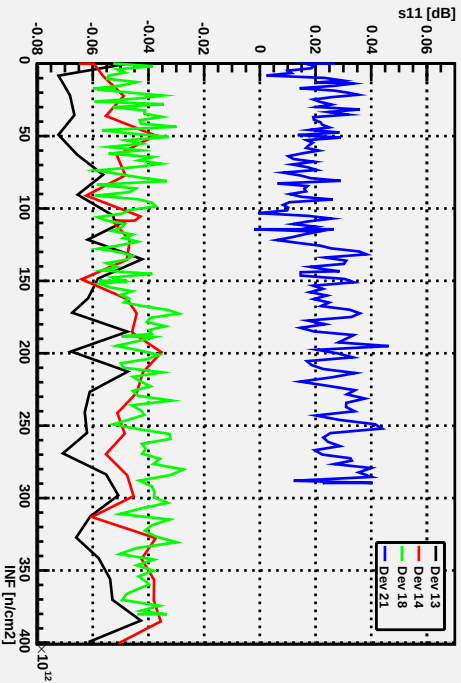
S21 vs. INF, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



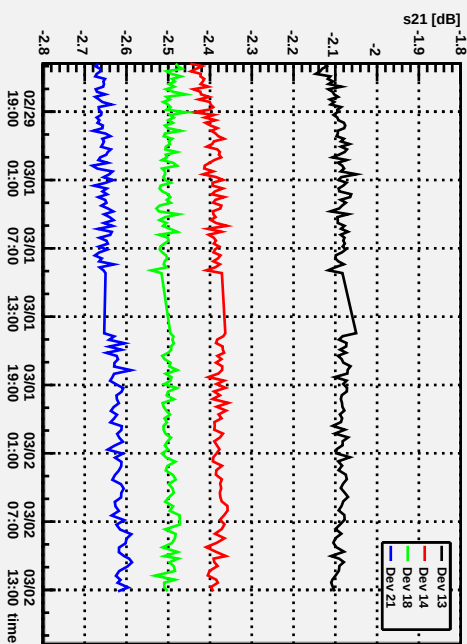
S11 vs. time, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



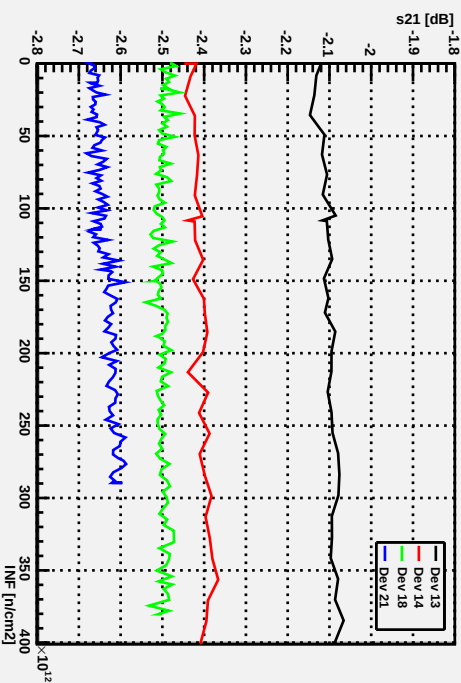
S11 vs. INF, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



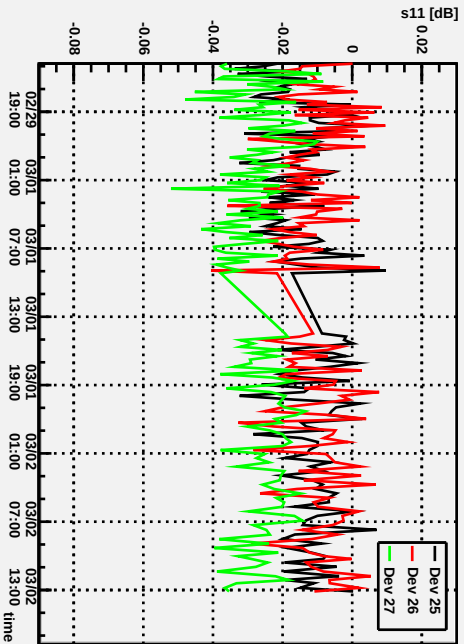
S21 vs. time, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



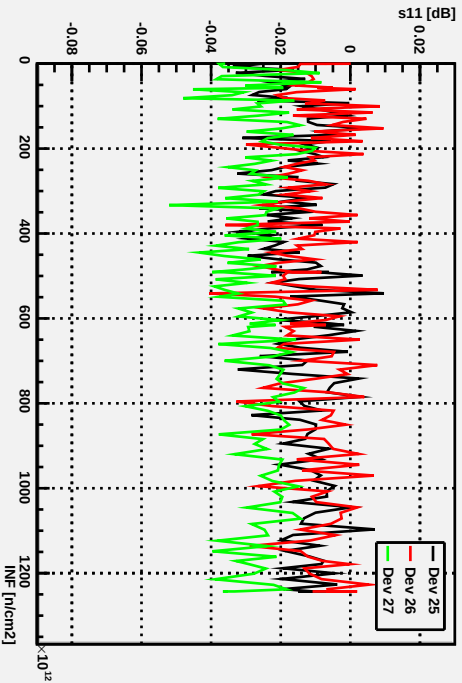
S21 vs. INF, at 80 MHz, Dev 13, 14, 18, 21 (IHP Sige trans., n-type)



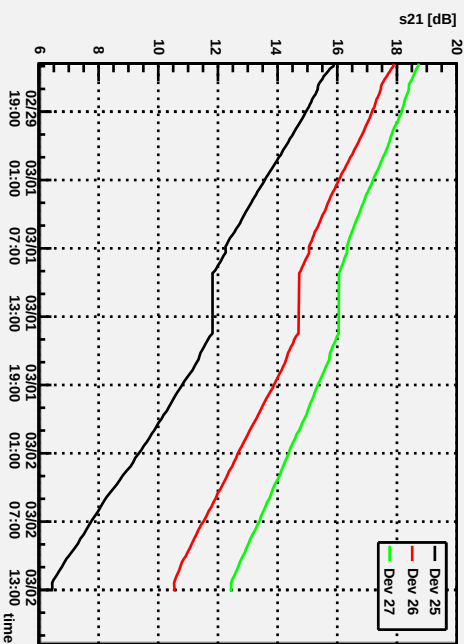
S11 vs. time, at 20 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



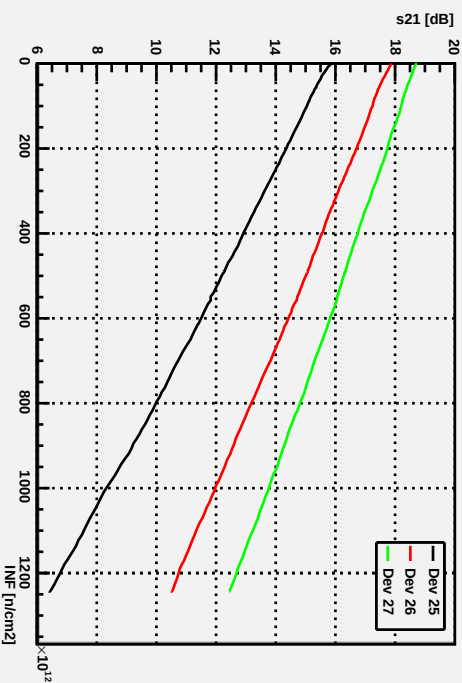
S11 vs. INF, at 20 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



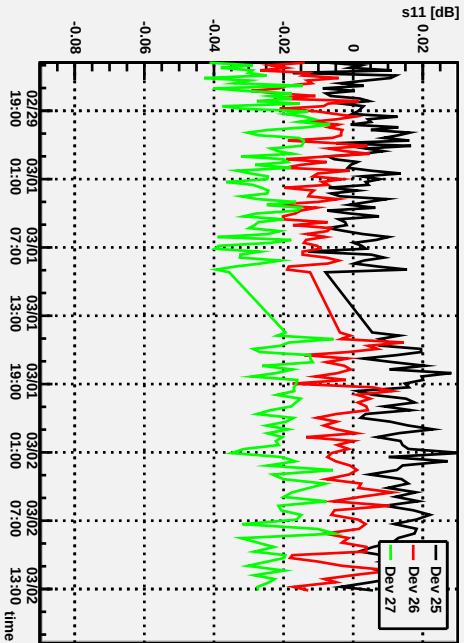
S21 vs. time, at 20 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



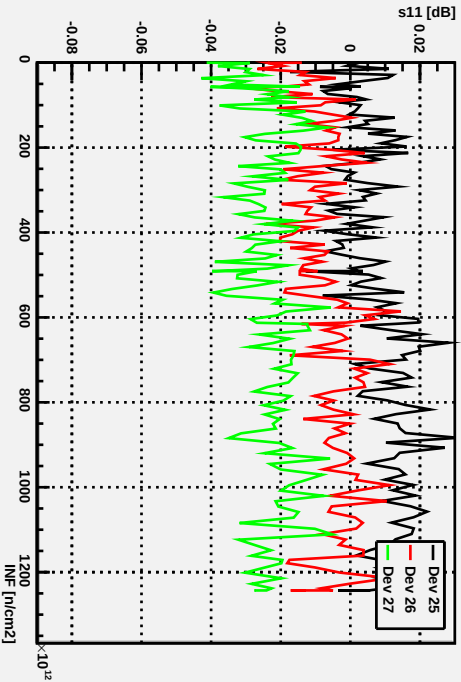
S21 vs. INF, at 20 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



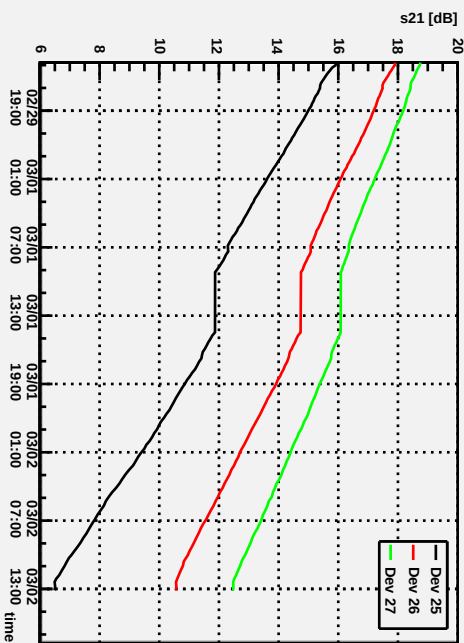
S11 vs. time, at 40 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



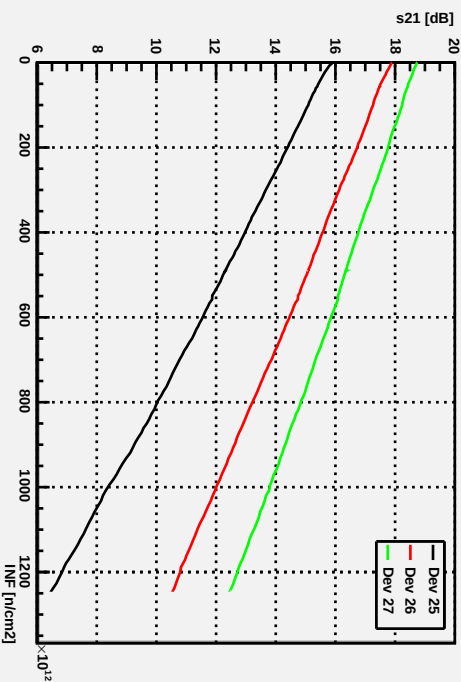
S11 vs. INF, at 40 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



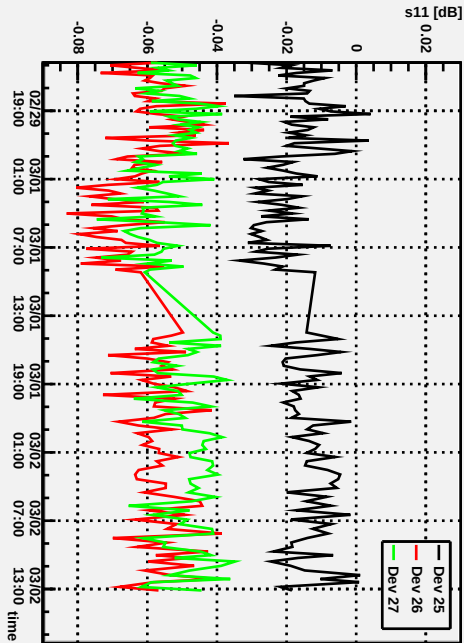
S21 vs. time, at 40 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



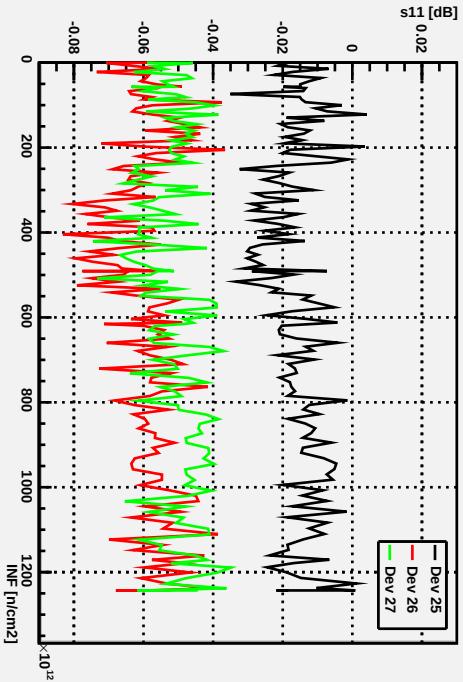
S21 vs. INF, at 40 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



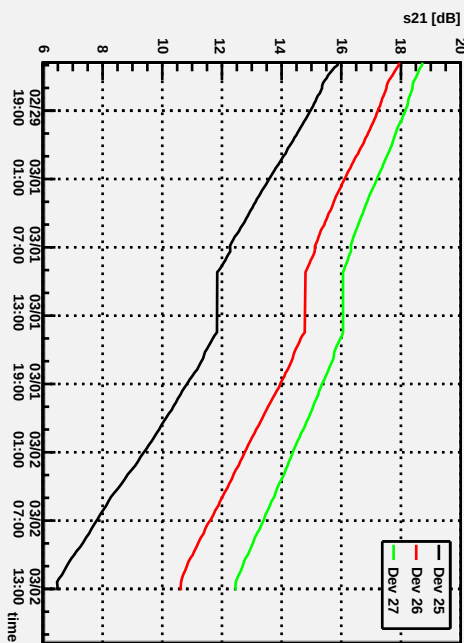
S11 vs. time, at 80 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



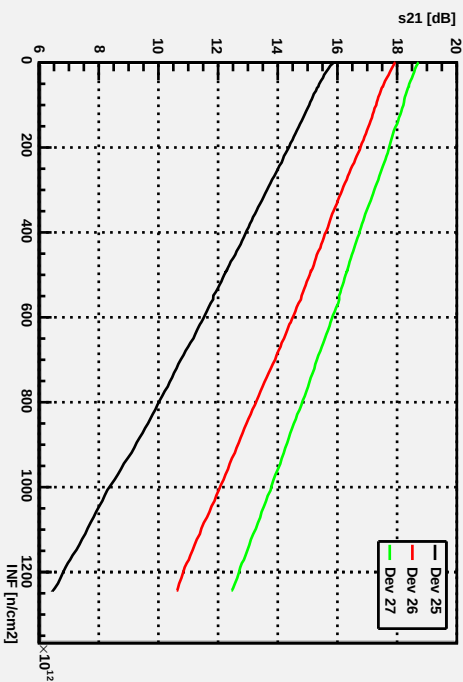
S11 vs. INF, at 80 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



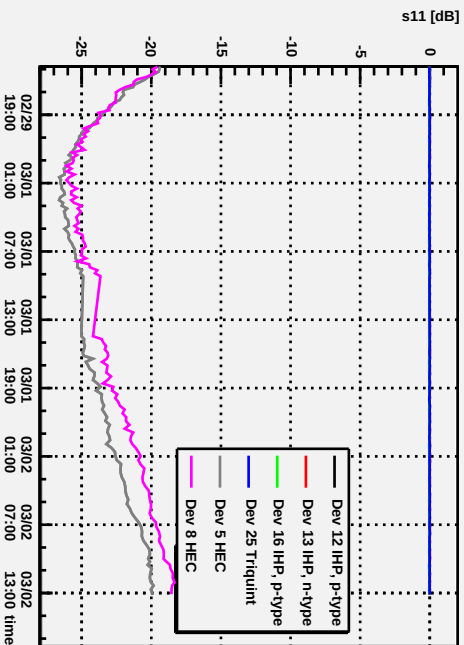
S21 vs. time, at 80 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



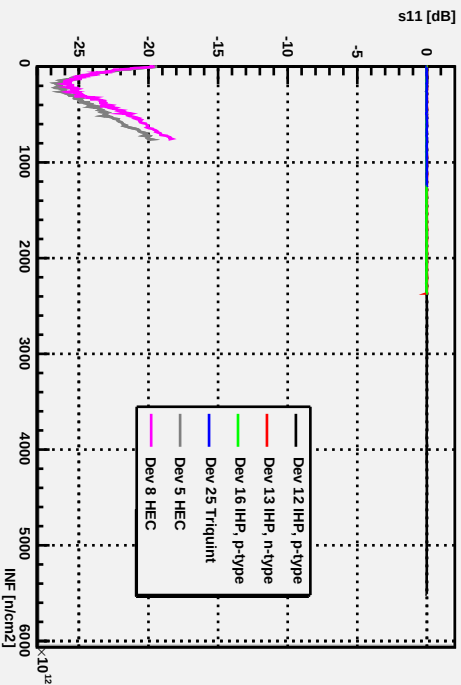
S21 vs. INF, at 80 MHz. Dev 25, 26, 27 (GaAs Triquint trans.)



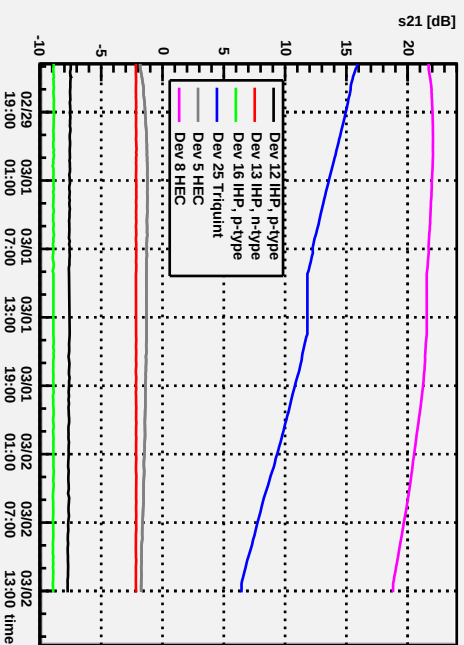
S11 vs. time, at 20 MHz. All technologies together (diff. slots)



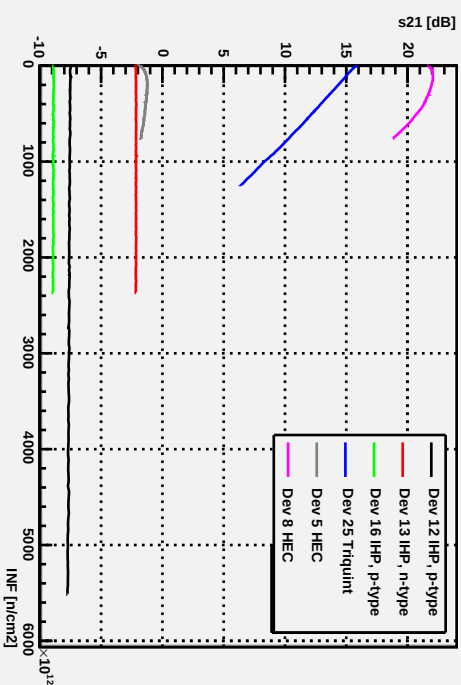
S11 vs. INF, at 20 MHz. All technologies together (diff. slots)



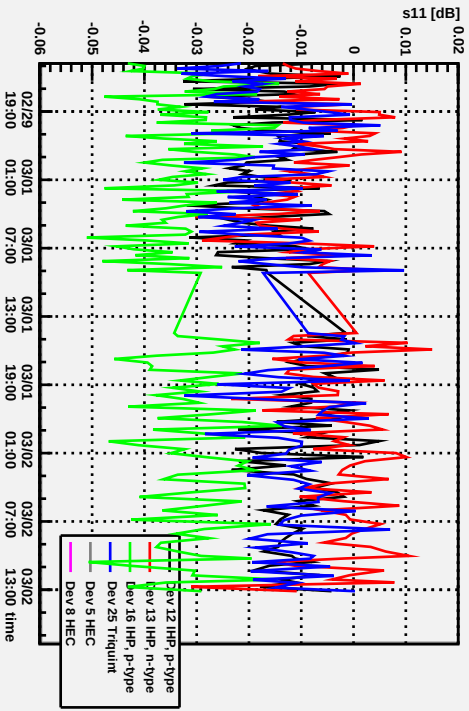
S21 vs. time, at 20 MHz. All technologies together (diff. slots)



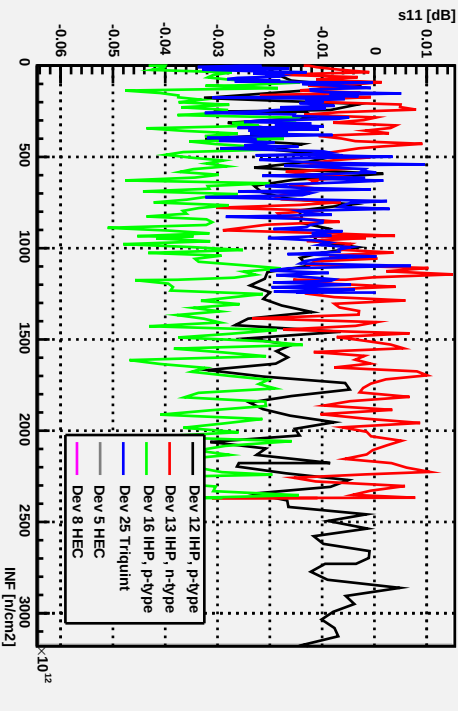
S21 vs. INF, at 20 MHz. All technologies together (diff. slots)



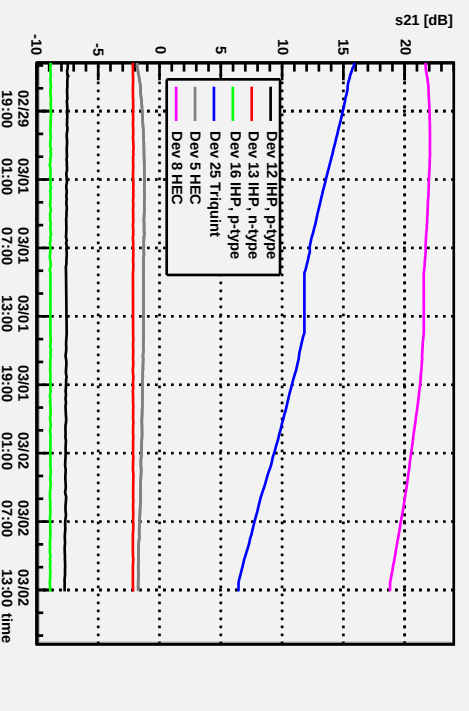
S11 vs. time, at 20 MHz. All technologies together (diff. slots)



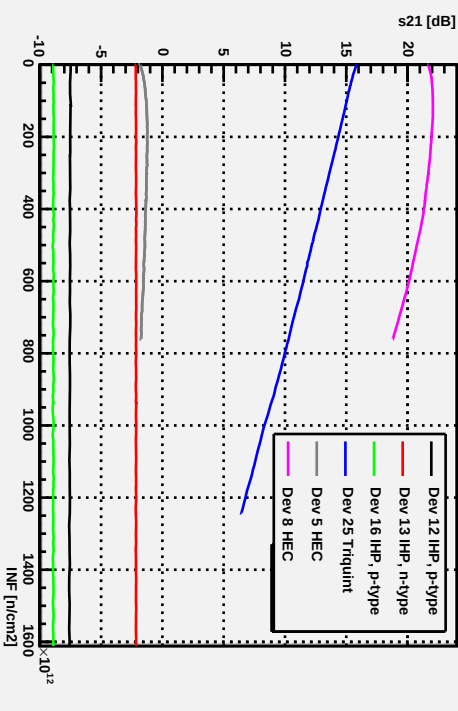
S11 vs. INF, at 20 MHz. All technologies together (diff. slots)



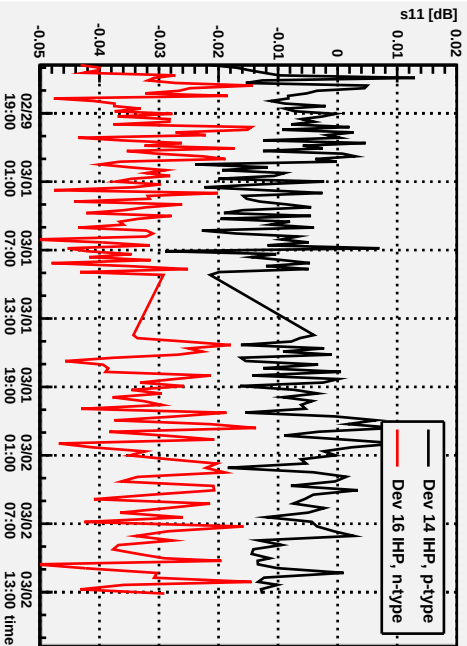
S21 vs. time, at 20 MHz. All technologies together (diff. slots)



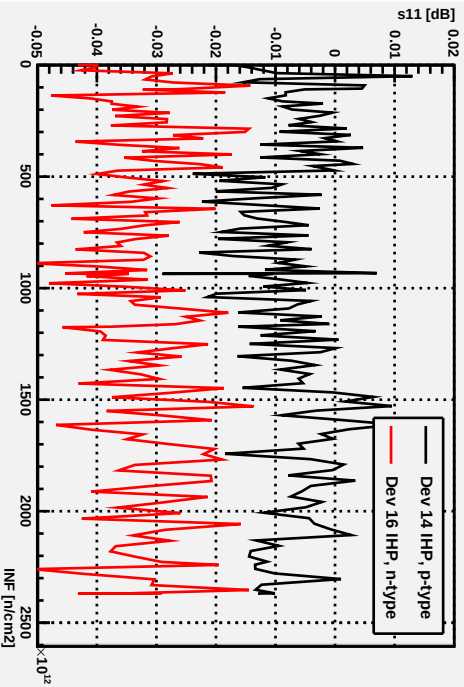
S21 vs. INF, at 20 MHz. All technologies together (diff. slots)



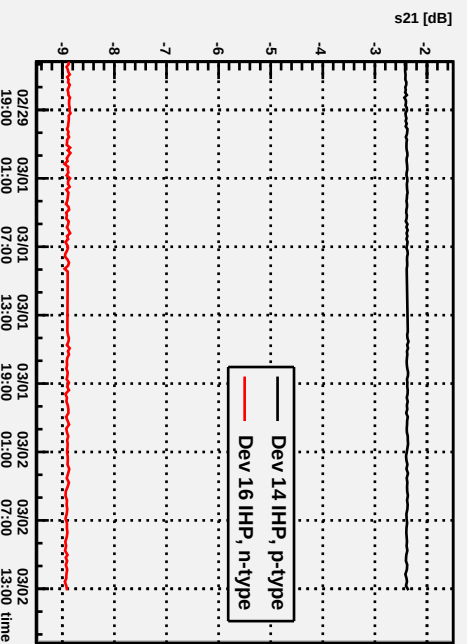
S11 vs. time, at 20 MHz, Dev 14 & 16 (IHP n & p. type, both in slot 2)



S11 vs. INF, at 20 MHz, Dev 14 & 16 (IHP n & p. type, both in slot 2)



S21 vs. time, at 20 MHz, Dev 14 & 16 (IHP n & p. type, both in slot 2)



S21 vs. INF, at 20 MHz, Dev 14 & 16 (IHP n & p. type, both in slot 2)

